

Fig. 1

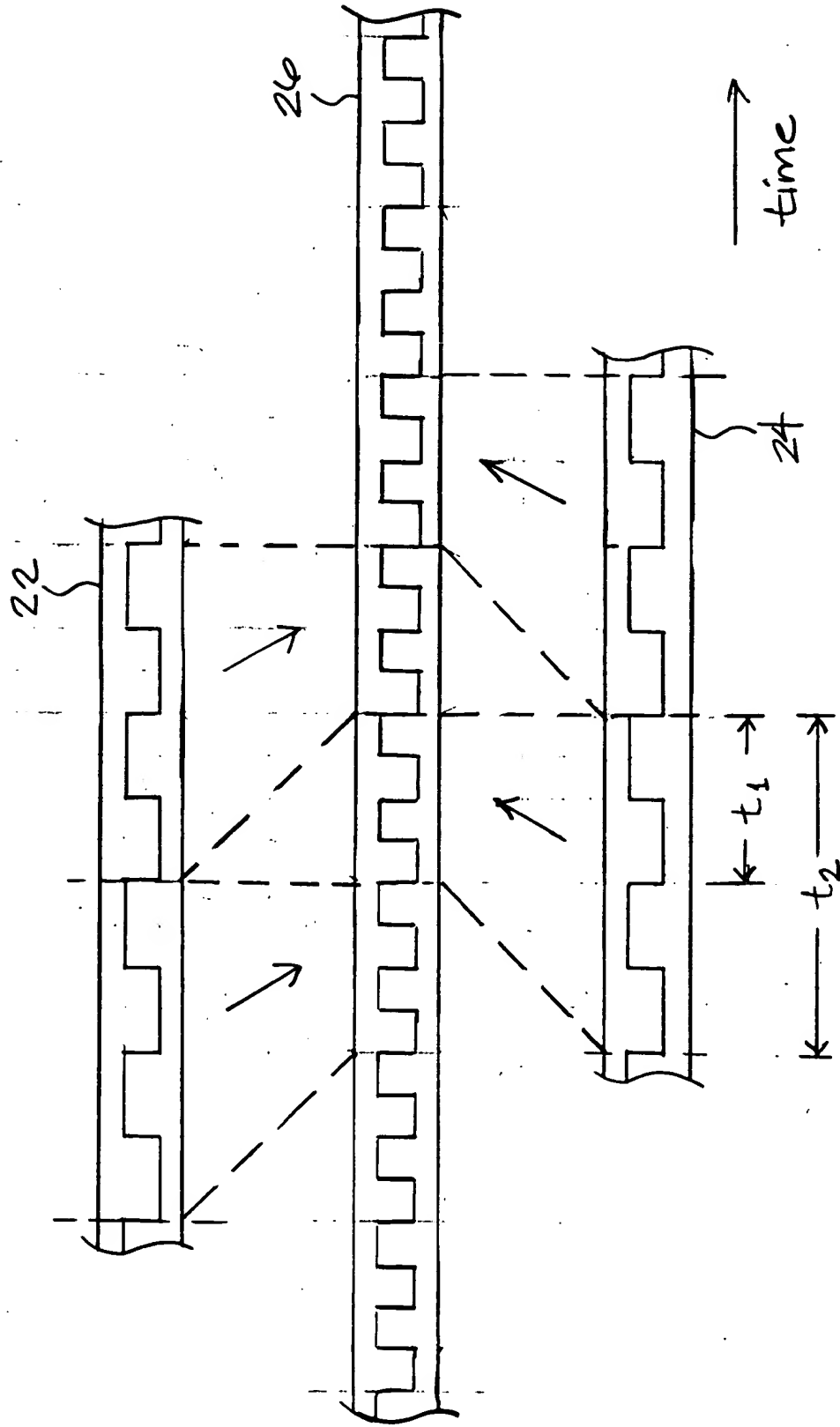


Fig. 2

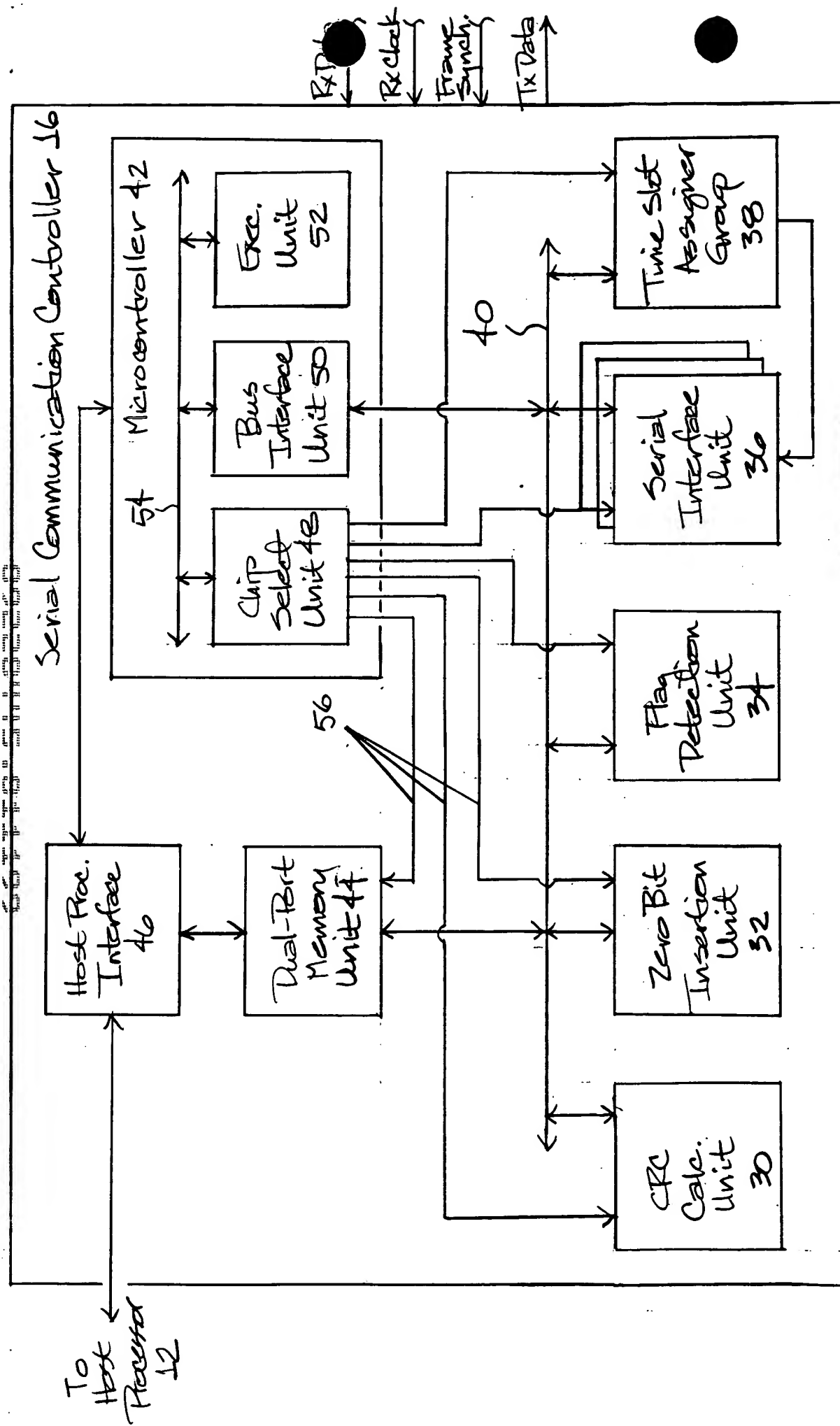


Fig. 3

Opening Flag Field	Address Field	Control Field	Information Field	Frame Check Sequencing Field	Closing Flag Field
01111110	8bits	8bits	8*n bits	16/32bits	01111110

Fig. 4

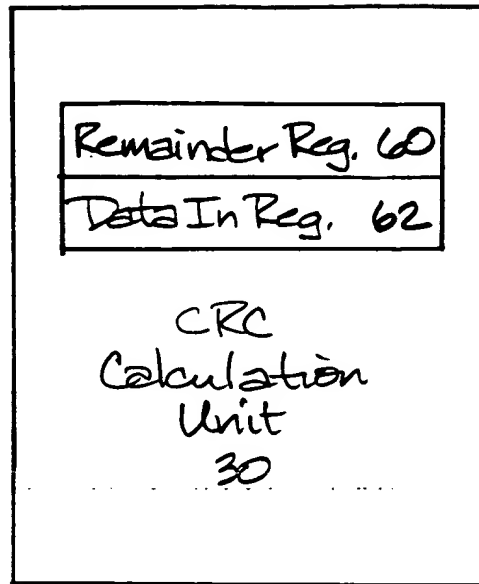


Fig. 5

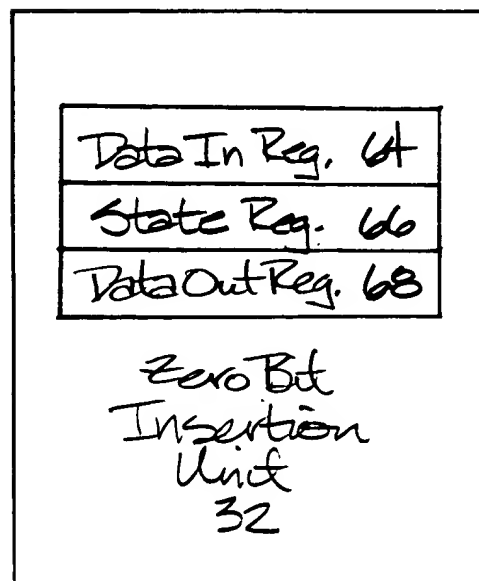


Fig. 6

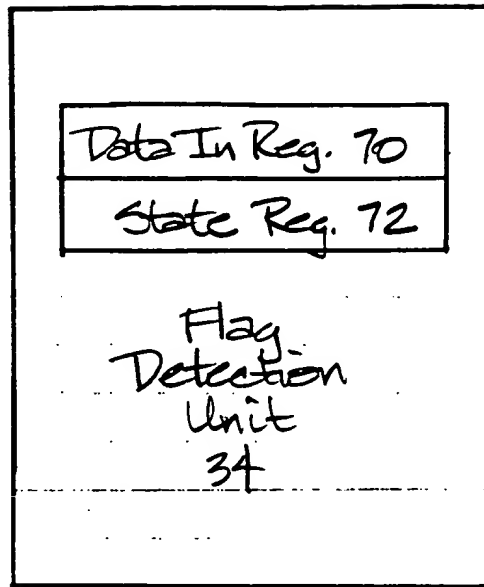


Fig. 7

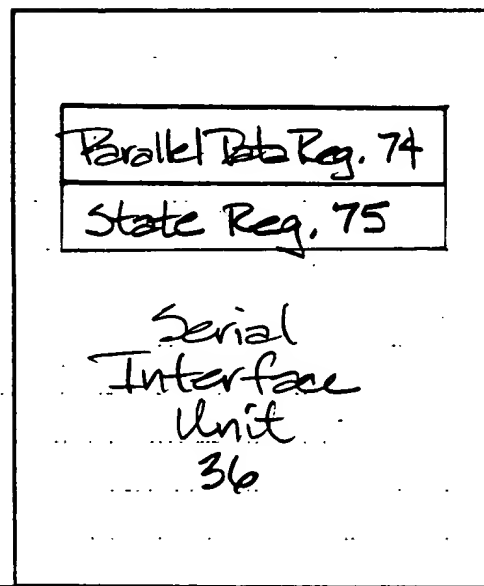


Fig. 8

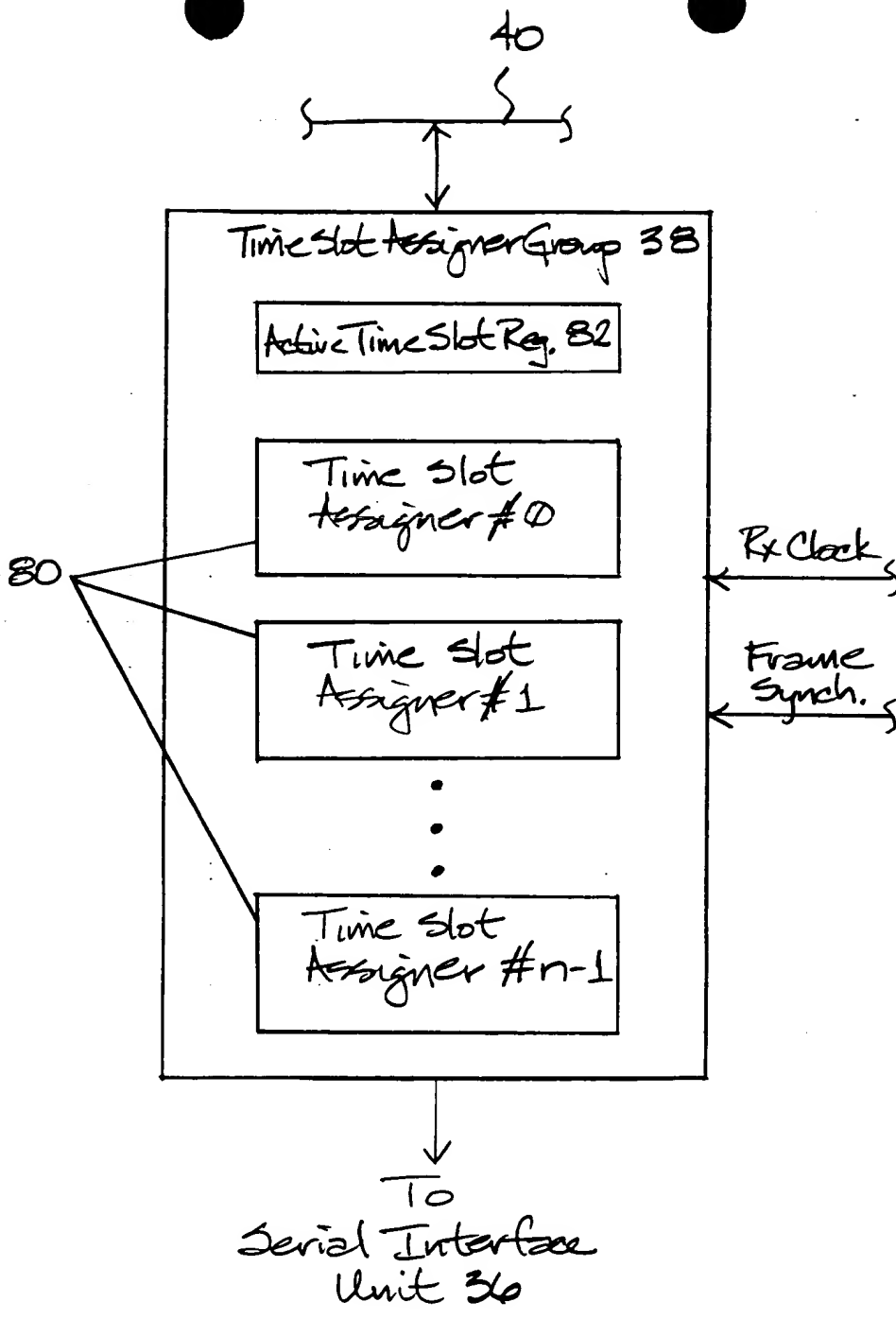


Fig. 9

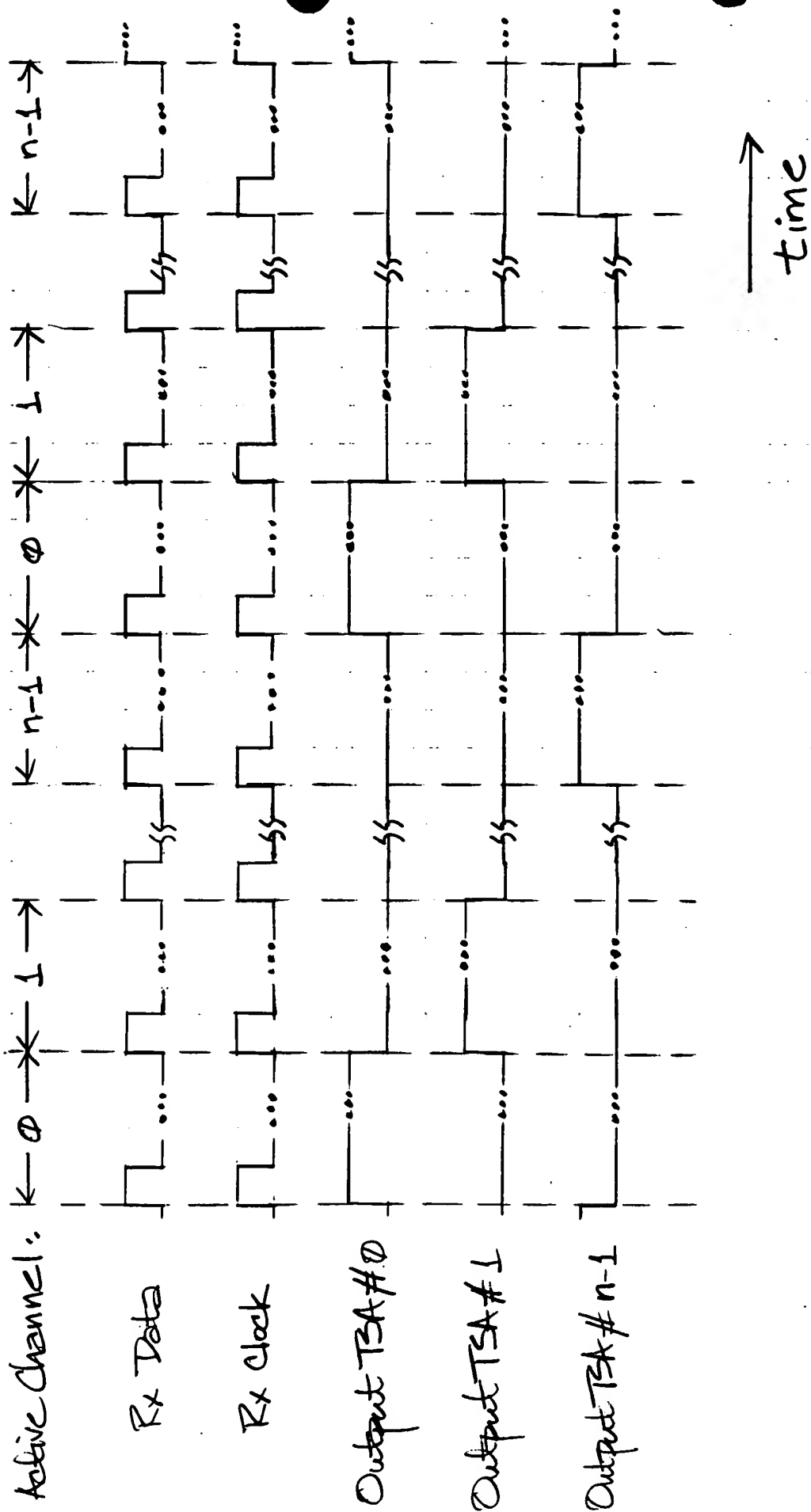


Fig. 10



# Dual-Port Memory Unit 44

Channel:

0

State/Data Buffer

Parallel Data Buffer

1

State/Data Buffer

Parallel Data Buffer

⋮

n-1

State/Data Buffer

Parallel Data Buffer

Instruction Store

82

84

Fig. 11